

## RADAR SENSOR AND METHOD FOR OPERATING A RADAR SENSOR

Field of the Invention

The present invention is directed to a method for operating a radar sensor whose radar pulses are generated by passing a continuous microwave signal through an RF switch which is periodically controlled by a pulse signal.

Background Information

10 *Introduction to Radar Systems*, by Merrill I. Skolnik, 2<sup>nd</sup> edition, McGraw-Hill Book Company, pp. 160 and 161, describes radar pulses being emitted for a mono-pulse tracking radar, and the radar pulses reflected from objects being supplied to a mixer. The mixed received pulses are supplied to an  
15 amplitude detector for obtaining a range signal, and to a phase detector for obtaining an angular error signal.

Published German patent document DE 199 63 005 describes a continuous microwave signal being passed through an RF switch,  
20 which is periodically controlled by a pulse signal, to generate radar pulses. The receive-side mixer also receives a continuous microwave signal passed through an RF switch as a reference signal. The pulse signal for controlling the receive-side RF switch is delayed by the propagation time of  
25 the reflected radar pulse.

Summary

The modulation of the pulse signal according to the present invention for controlling the RF switch in the transmission branch expands the spectral lines of the pulse signal without occurrence of decorrelation. As a result, regardless of the frequency drift, always the same noise level, which is approximately equal to the minimum achievable noise level  
35 regardless of the relative position of the LO (local

oscillator) frequency and PRF (pulse repeat frequency), appears on the receive side after mixing the radar signal in the CF position. The critical case of  $f_{LO} = n \cdot PRF$  is thus circumvented. Due to this optimization of the noise level, the detection range of the short-range radar (SRR) may be increased, e.g., from 7 m to 14 m, without the received signals of the objects to be detected getting lost in the noise depending on the characteristics of the object, which would make reliable detection impossible.

The edge steepness and thus the harmonic components of the baseband pulse may be reduced by appropriate filtering of the input signal and/or output signal of the RF switch.

The harmonics may also be reduced by using diodes having a linear characteristic, e.g., PIN diodes, as RF switches. The required rapid switching times for the SRR, e.g., 400 ps for radar pulses, may be achieved using PIN diodes having a very thin intrinsic layer.

The noise factor may be further reduced by using diode pairs in the receive mixer, i.e., phase detector, and also in the transmission-side RF switch. In particular, the phase detector may advantageously have a diode pair on a chip within a ring mixer having strip conductors.

An oscillator for controlling the RF switch, oscillating in a clean mode and having an output-side buffer amplifier operated at saturation, may be provided to reduce the amplitude noise.

In contrast with a previously mentioned mono-pulse tracking radar known in the art, in the method according to the present invention a normal pulse radar having extremely short pulses is used.

Figure 1 shows a block diagram of a radar sensor according to the present invention.

Figure 2 shows a comparison of the baseband pulse spectrum and receive pulse spectrum.

5 Figures 3 and 4 show the demodulation of the phase jitter of the baseband pulse spectral line and the adjacent receive pulse spectral line within the CF bandwidth.

Figure 5 shows the power of a baseband pulse spectral line  
10 plotted against the RF pulse width both with and without modulation of the pulse signal for controlling the transmission-side RF switch.

Figure 6 shows the transmission-side RF switch having pulse  
15 shaping.

Figure 7 shows the power of a baseband spectral line plotted against the RF pulse width both with and without baseband pulse shaping.

20 Figure 8 shows the noise factor plotted against the RF pulse width using Schottky and PIN diodes.

Figure 9 shows a schematic illustration of a layout for a  
25 phase detector.

Figure 10 shows the noise factor plotted against the RF pulse width using sources having different amplitude noise.

### 30 Detailed Description

An oscillator 3 for microwave signals, which delivers a continuous high-frequency signal (CW signal), is provided in the radar sensor of Figure 1. This high-frequency signal is  
35 supplied, via a signal splitter in the form of hybrid circuit 4, to the input of a transmission-side RF switch 1 for transmitting radar pulses to transmission antenna 5 and to a

receive-side RF switch 2, which controls a receive mixer 7 in the signal path to a receive antenna 8. Transmission-side RF switch 1 is controlled by control unit 9, which has a pulse signal source 10, and a delay circuit 11. If pulse signal source 10 delivers a pulse, the high-frequency wave of oscillator 3 is supplied, for the duration of this pulse, to the transmission antenna via RF switch 1. The echo signal reflected from an object is supplied to mixer 7, which receives a reference signal via switch 2 if delay circuit 11 is set for a certain range in which the objects are to be detected according to a desired time gate. If the same high-frequency sources are applied to both inputs of mixer 7, a CF output signal, proportional to the coincidence, is obtained, which is further processed in an analyzer circuit 12.

The SSR system is typically operated using a pulse repeat frequency PRF of 5 MHz for pulse signal source 10, for example, which means that a pulse is emitted every 200 ns. The PRF is derived from a very pure quartz and has negligible phase jitter. The control pulse, referred to hereinafter as baseband pulse, for the RF switch has a width of 400 ps. The spectrum of the baseband pulse corresponds to the Fourier transform of the pulse in the time range and has a  $\sin(x)/x$  shape around 0 Hz and spectral lines spaced at the pulse repeat frequency. RF switch 1 unintentionally operates as a harmonic multiplier for the baseband pulse, because the switch output is not band-limited. Therefore, the spectral lines of the baseband pulse are multiplied beyond 24.125 GHz. The baseband pulse spectral lines are highly expanded by this multiplication, i.e., have a very high phase jitter around 24.125 GHz, although they are derived from a very pure source. This undesirable, highly multiplied baseband pulse spectrum and the desired spectrum, which is obtained by modulating the 24.125 GHz CW signal using the baseband pulse and represents the actual receive pulse, now overlap around 24.125 GHz. Due to the fact that the baseband pulse spectral lines have a very high phase jitter, both spectra are demodulated in the phase

detector because detectable phase jitter components of the baseband pulse spectral lines occur even at  $\pm PRF/2$  and in the CF band widths of the receive pulse spectral lines. Because not only one spectral line pair, but all available pairs are similarly demodulated with respect to one another, the CF noise and therefore the system noise factor increase considerably. Although there is an optimum distance of  $PRF/2$  of the baseband pulse spectrum from the receive pulse spectrum, the RF frequency will drift with the temperature; therefore, the noise factor will change considerably and, in the most unfavorable case, perfect overlap and sporadic noise will occur.

Since the demodulation of the phase jitter of the baseband pulse spectral lines cannot be prevented using the receive pulse spectral lines, and, in particular, the fluctuations of the noise with the temperature are barely controllable, according to the present invention frequency modulation (PRF modulation) of the pulse signal is used for controlling RF switch 1 via modulator stage 20, which ensures, regardless of the frequency drift, that the same noise level, approximately corresponding to the minimum achievable noise level, always occurs at the CF output. The PRF is frequency-modulated, the frequency shift being 1 kHz, for example, and the modulation frequency being 10 kHz, for example. The low modulation index of 0.1 ensures that the radar signal does not decorrelate by itself. Harmonic multiplication also multiplies the modulation index; a factor 4,850 is required to get, for example, from 5 MHz to 24.125 GHz. This results in an intended, extreme expansion of the individual base pulse spectral lines. This extreme expansion results in an almost flat baseband pulse spectrum. Thus, independent of the frequency offset of  $n \cdot PRF$  with respect to the RF, the CF bandwidths of the individual receive pulse spectral lines have the same power. This flatness therefore results in a constant noise factor, which is almost equal to the optimum noise factor at optimum frequency offset.

Figure 2 shows the comparison between baseband pulse spectrum and receive pulse spectrum, i.e., baseband pulse spectrum 13 and LO spectrum 14 modulated using the carrier. Figures 3 and 4 are enlarged graphs of the baseband pulse spectral line phase jitter, demodulated using the adjacent receive pulse spectral line within the CF bandwidth. Reference symbol 15 identifies the expanded phase jitter of the highly multiplied PRF spectral lines, and reference symbol 16 identifies the CF bandwidth in which signals may be demodulated. Reference symbol 17 shows how highly multiplied phase jitter falls into the CF bandwidths and is demodulated. Figure 5 shows the power of a baseband pulse spectral line at  $n \cdot \text{PRF} = 24.125 \text{ GHz}$  plotted against the RF pulse width in ps with (reference numeral 18) and without (reference numeral 19) PRF modulation. An improvement by 30 dB may be observed in the most unfavorable case of perfect overlap.

Because the baseband pulse spectral lines have an extreme phase jitter, this phase jitter is demodulated via the spectral lines of the receive pulse. This phenomenon occurs for each spectral line pair and is added up without correlation. This demodulated signal has the character of noise and increases the CF noise and thus the system noise factor. By reducing the harmonics of the baseband pulse around 24 GHz, the noise factor may be further reduced. The power of the harmonics of the baseband pulse around 24 GHz does not depend on the baseband pulse width, but rather on the edge steepness of the pulse. The edge steepness and thus the harmonics may be reduced by appropriate pulse shaping. One approach involves low pass filtering, which may be achieved, for example, by using a 10 pF capacitor at the input of RF switch 1. Another option is the use of a Gaussian filter or another filter for more exact pulse shaping and more controlled harmonics reduction, and thus improving the noise factor.

The demodulation mechanism of the baseband and receive pulse spectral lines is shown in Figures 2 through 4. Figure 6 shows one possible implementation of pulse shaping by filtering. RF switch illustrated in Figure 6 has a capacitor 23 for pulse shaping (low-pass) between control input 21 for the baseband pulse, which may be frequency modulated, and ground terminal 22. After undergoing a  $\lambda/4$ -transformation at element 29, the baseband pulse is supplied to a diode pair 24, 25 which is connected to ground 28. In the conducting state, the diode pair 24, 25 connects, in the form of finger couplers, the output signal of oscillator 3 applied to input 30 to transmission antenna 5 via blocking circuits 26, 27 and output 31 for a baseband pulse. Transformation circuits 32 and 33 form RF short circuits. Switch 1 has a strictly symmetric design except for the side branch in the form of capacitor 23. Therefore, it allows an insulation resistance (conducting state/blocking state) of 50 dB to be achieved.

Figure 7 shows the measurement results of the baseband pulse spectral line at 24.125 GHz: Power/dBm against pulse width/ps with (reference numeral 24) and without (reference numeral 25) pulse shaping. As is evident from Figure 7, an improvement of 10 dB in the noise factor is achievable.

The phase jitter generated by the baseband spectral lines may be reduced by reducing the harmonics of the baseband pulse around 24 GHz. These harmonics are generated in RF switch 1, for example, by two Schottky diodes. These diodes are used because they switch very rapidly due to the physical characteristics of the Schottky metal layer. However, they have an extremely non-linear characteristic, which makes them suitable for mixer applications. This non-linearity is very disadvantageous for the switch, because it is this characteristic in particular that makes it possible to effectively generate the harmonics of the baseband pulse. To overcome these disadvantages, diodes having a linear characteristic, such as PIN diodes, for example, are used.

These diodes generate harmonics that are lower by 10 dB and result in a noise factor which, depending on the pulse shape, is also lower by this order of magnitude. The problem of inertia of these diodes with respect to the very rapid pulse of 400 ps (typical switching times  $>2$  ns are preferred) may be circumvented by using PIN diodes having very thin intrinsic layers (i-layers of less than  $2\text{ }\mu\text{m}$ ), which are able to switch more rapidly. GaAs PINs having very thin i-layers are able to switch rapidly up to 400 ps and are therefore suitable, while the predefined operating parameters of the SRR radar are preserved.

Figure 8 shows the reduction of the noise factor by using PIN diodes (reference numeral 34) compared to Schottky diodes (reference numeral 35).

The above-described SRR system, having a typical pulse repeat frequency of 5 MHz and a typical RF pulse width of 400 ps, has an output spectrum of the receive switch which is centered around the 24.125 GHz carrier frequency, for example, and has consecutive spectral lines spaced at the pulse repeat frequency and a shape corresponding to the Fourier transform of the RF pulse. Each individual spectral line is derived from the carrier, i.e., has the carrier's amplitude and phase jitter. This spectrum is supplied to the phase detector, i.e., receive mixer 7. A simply balanced phase detector theoretically provides perfect AM suppression, i.e., the amplitude noise of the local oscillator signal is extinguished in phase opposition. In practice, approximately 20 dB is achievable at a frequency of 24 GHz, for example. The phase detector thus may function as a poor AM demodulator. Because of the plurality of spectral lines, the individually demodulated amplitude noise is added up without correlation and is manifested in an increased noise level at the CF output, resulting in a higher noise factor.



To lower the system noise factor, e.g., the amplitude noise, it should be ensured that oscillator 3 oscillates in a clean mode." In addition, it is effective to place an insulator in the form of buffer amplifier 36, operated at saturation, downstream from oscillator 3 to ensure low amplitude noise. A clean voltage supply to buffer amplifier 36 is highly advantageous.

The noise factor may be further lowered by improving the balance of the phase detector. For this purpose, instead of using two physically separate diodes, two diode junctions (diode pair 37, 38) are used on a chip, which, according to Figure 9, is placed in the center within, rather than outside of, ring mixer 39. This has the advantage of the diode junctions being almost identical, which improves the balance. The use of an alternative phase detector, for example, a double-balanced detector having improved AM suppression, is also advantageous. The amplitude noise is shown in Figures 2 through 4. Figure 10 shows the system noise factor in the case of two different oscillators, a DRO (reference numeral 41) and a Gunn oscillator (reference numeral 40) which has a 10 dB lower noise. The noise factor may be lowered by a few dB.